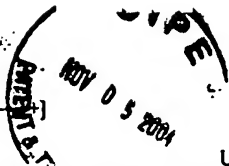


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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	10/728,933
				Filing Date	December 8, 2003
				First Named Inventor	Shunpei YAMAZAKI et al.
				Group Art Unit	2891
				Examiner Name	ZARNEKE
Sheet	1	of	1	Attorney Docket Number	0756-7225

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Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
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		JP	07-187890			07/25/1995	Full

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				First Named Inventor	
				Group Art Unit	
				Examiner Name	
Sheet	1	of	1	Attorney Docket Number	0756-7225

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AT		N. Hayasaka, "Recent Status of Thin Wafer Chip (die) Mounting," SEMICON JAPAN 2002, Dec. 5, 2002, pages 3-8	

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